



01/06

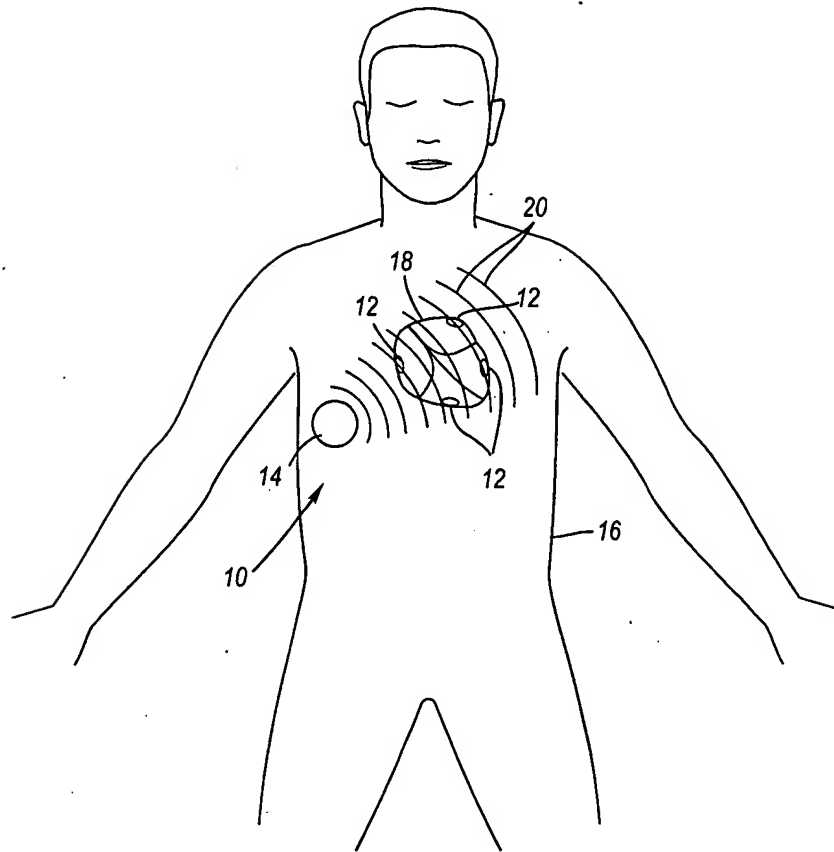


FIG. 1

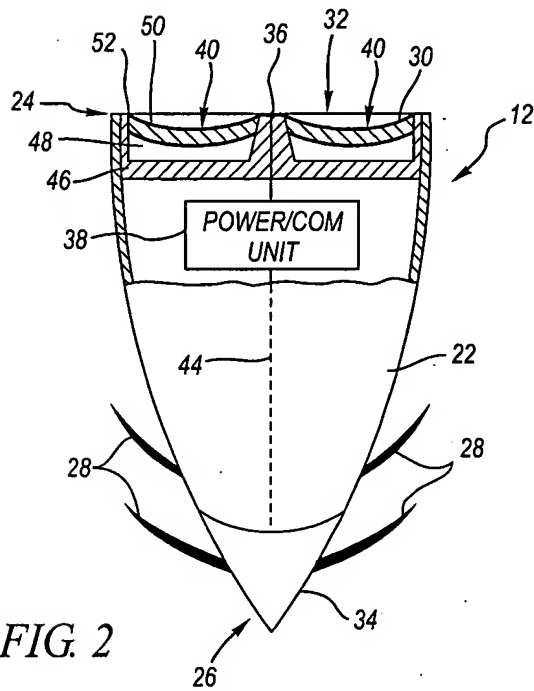


FIG. 2

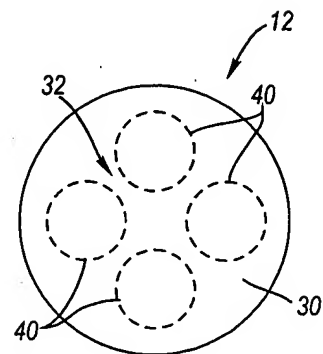


FIG. 3

```

graph TD
    32[ACOUSTIC TRANSDUCER ARRAY] --> 54[RECTIFIER]
    32 --> 60[SIGNAL DETECT CIRCUITRY]
    54 --> 56[ENERGY STORAGE DEVICE]
    60 --> 66[SWITCH]
    60 --> 58[CONTROLLER]
    56 --> 66
    66 --> 58
    66 --> 34[STIMULATION ELECTRODE]
    58 <--> 64[MEMORY]
    58 --> 62[TRANSMISSION CIRCUITRY]
    62 --> 34
    subgraph 38 [ ]
        54
        56
        60
        66
        58
        62
        64
    end

```

FIG. 4

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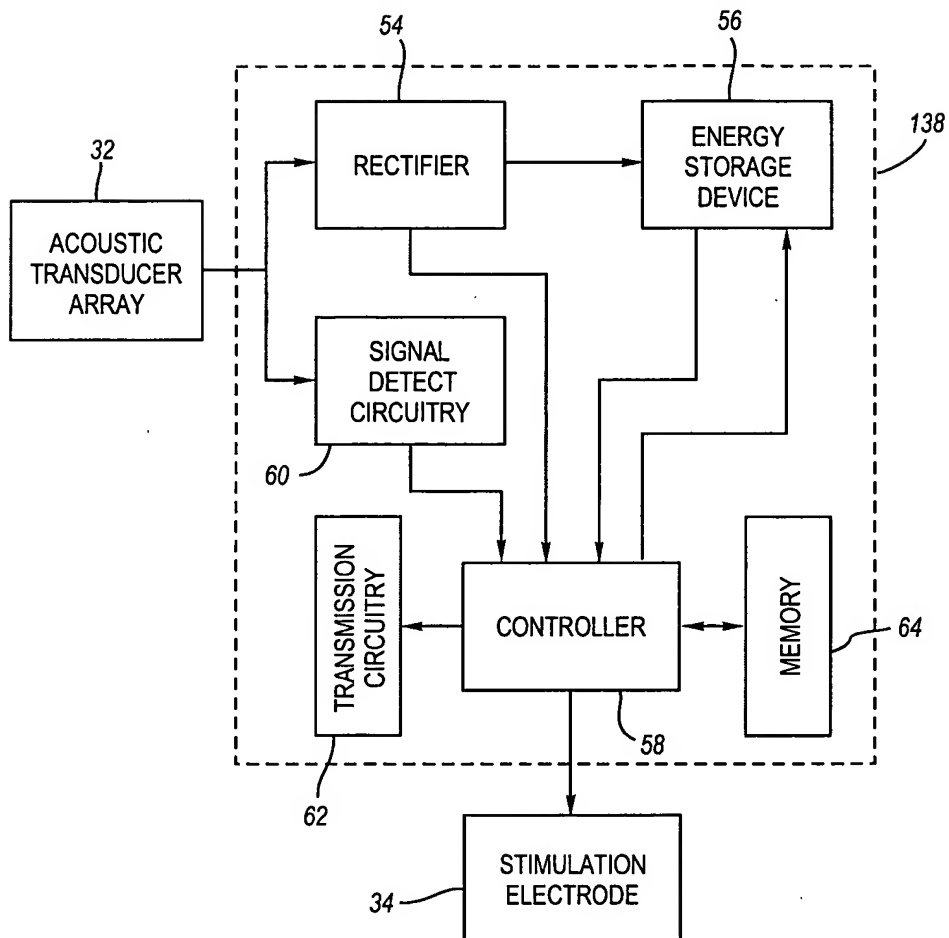


FIG. 5

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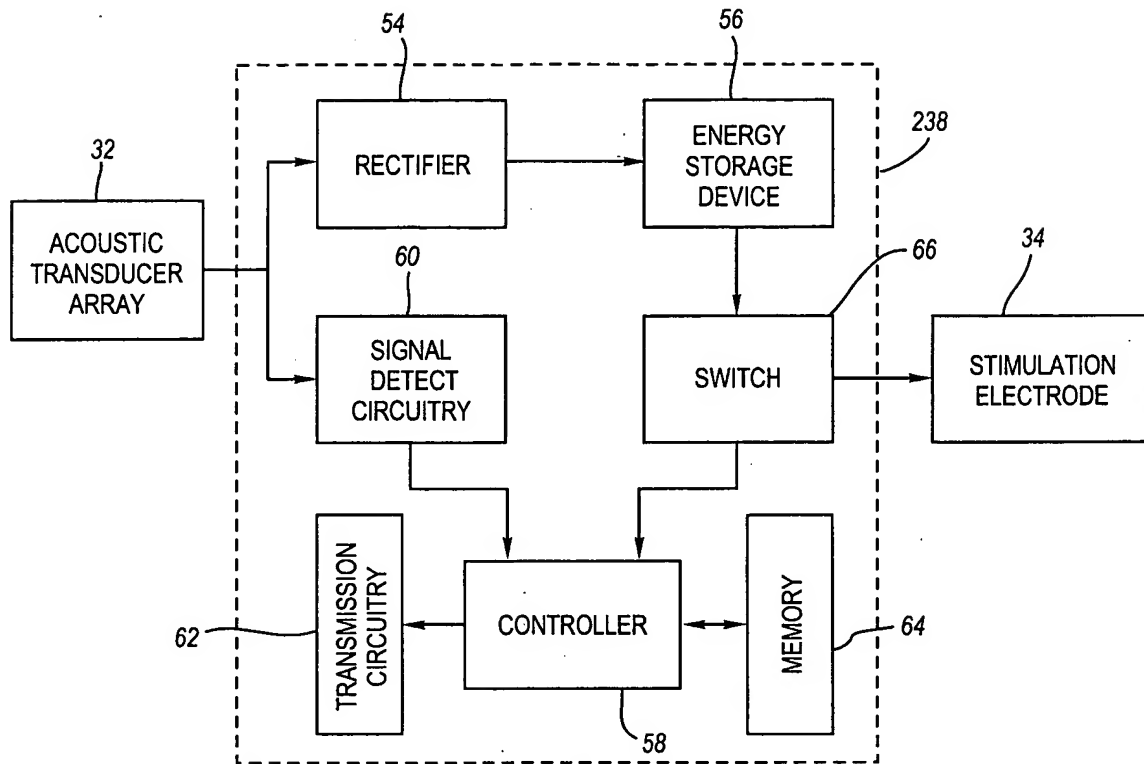


FIG. 6

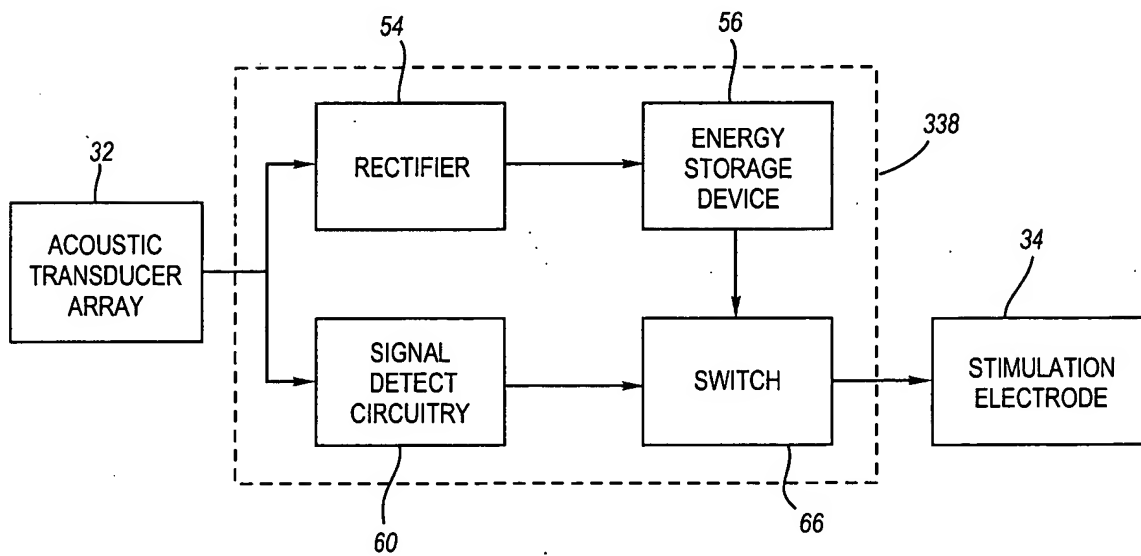


FIG. 7

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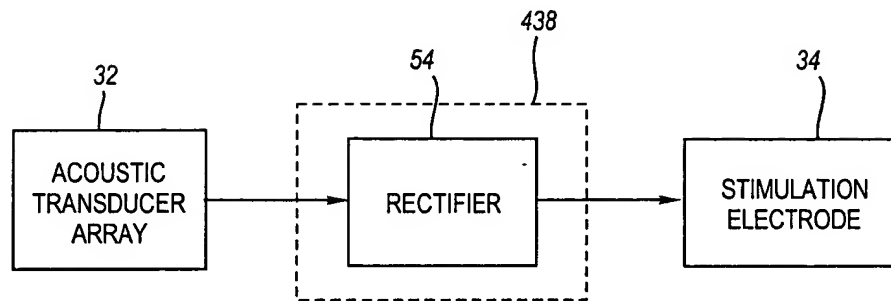


FIG. 8

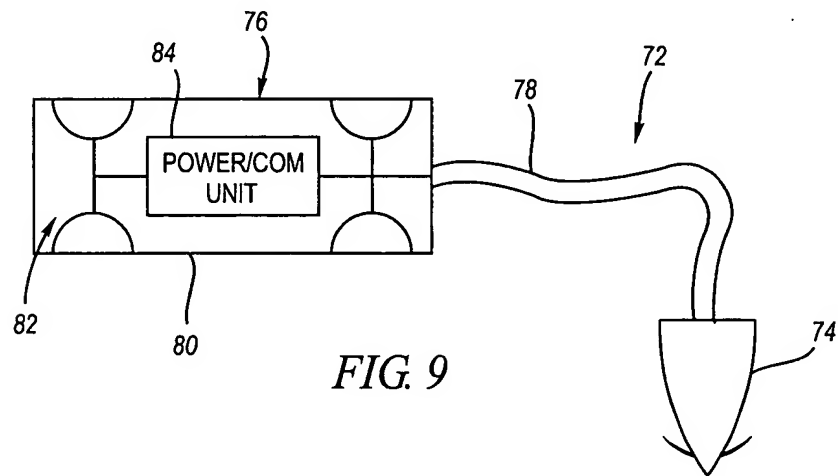


FIG. 9

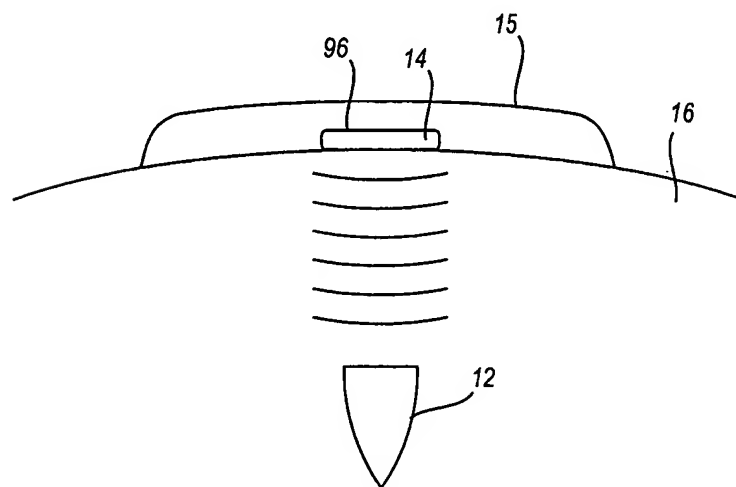


FIG. 10

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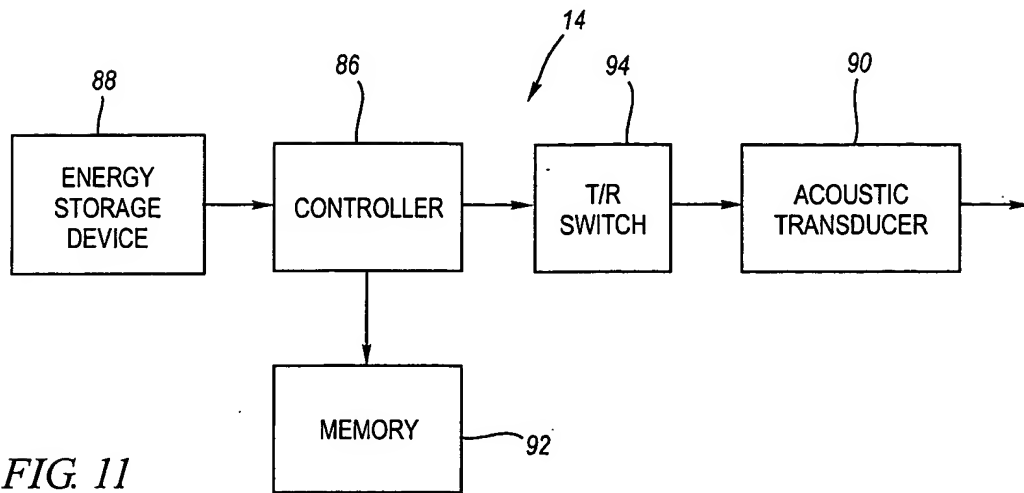


FIG. 11

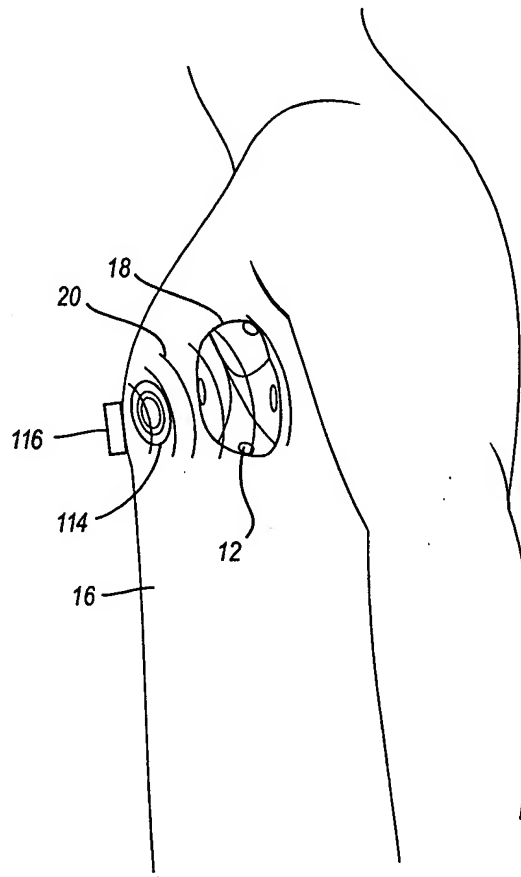


FIG. 12